Docket No.: M4065.0254/P254-

В

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Kie Y. Ahn, et al.

Application No.: Not Yet Assigned

Filed: August 6, 2003 Art Unit: 2836

For: MICROTRANSFORMER FOR SYSTEM-

ON-CHIP POWER SUPPLY

Examiner: S. Jackson

INFORMATION DISCLOSURE STATEMENT (IDS)

MS Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith.

Those patents or publications which are marked with an asterisk (*) in the attached form PTO/SB/08 are not supplied because they were previously cited by or submitted to the Office in a prior application no. 09/466,134, filed December 21, 1999, which is a continuation-in-part of application no. 09/458,089, filed December 10, 1999

now abandoned, and relied upon in this application for an earlier filing date under 35 U.S.C. 120.

A concise explanation of relevance of the items listed on form PTO/SB/08 is:

[x]]not given
[]	given for each listed item
[]	given for only non-English language listed items
[]	in the form of an English language copy of a Search Report from a foreign patent office, issued in a counterpart application, which refers to the relevant portions of the references

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. In accordance with 37 CFR 1.97(h), the filing of this Information Disclosure statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0254/P254-B.

Dated: August 6, 2003

Respectfully submitted,

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000		. •		Application Number	Not Yet Assigned	
INFORMATION DISCLOSURE				Filing Date	August 6, 2003	
STATEMENT BY APPLICANT			APPLICANT	First Named Inventor	Kie Y. Ahn	
				Art Unit	2836	
	(Use as many si	heets as	necessary)	Examiner Name	S. Jackson	
Sheet	1	of	1	Attorney Docket Number	M4065.0254/P254-B	

	U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.1	Document Number Number-Kind Code² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
_	*AA	5,070,317	12/3/1991	Bhagat		
	*AB	5,279,988	1/18/1994	Saadat.et al.		
	*AC	5,519,582	5/21/1996	Matsuzaki		
	*AD	5,583,474	12/10/1996	Mizoguchi et al.		
	*AE	5,852,289	12/22/1998	Masahiko		
	*AF	5,942,965	08/24/1999	Kitamura et al.		
	*AG	6,376,909	04/23/2002	Forbes et al.		

FOREIGN PATENT DOCUMENTS						
Examiner	Cite No.1	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
Initials*		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				T ⁶

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. \(^1\)Applicant's unique citation designation number (optional). \(^2\)See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. \(^3\)Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). \(^4\)For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. \(^5\)Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. \(^6\)Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS							
Examiner Initials	Cite No.1						
	*CA	Mimura et al.; "System module: a new Chip-On-Chip module technology"; IEEE, 1997, Custom Integrated Circuits Conference, pages 439-442.					
	*CB	Yan et al.; "Reducing Operating Voltage from 3, 2, to 1 Volt and Below – challenges and guidelines for possible solutions"; IEEE, 1995, IEDM, pages 55-58.					
	*CC	Christensen et al.; "Wafer Through-Hole Interconnections with High Vertical Wiring Densities", IEEE Transactions on Components, Packing, and Manufacturing Technology-Part A, Vol. 19, No. 4, Dec. 1996, pages 516-522.					
	*CD	Mimura et al.; "System module: a new Chip-On-Chip module technology"; IEEE , 1997, Custom Integrated Circuits Conference, pages 439-442.					

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached.